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PATENT NUMBER and
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U.S. UTILITY Patent Application

10/048201

APPL NUM 10048207	FILING DATE 01/28/2002	CLASS 438	SUBCLASS 1.1	GAU 1765	EXAMINER J. L. L. L.
**APPLICANTS: Heineck Lars-Peter; Jacobs Tobias; Winnerl Josef;					
**CONTINUING DATA VERIFIED: THIS APPLICATION IS A 371 OF PCT/DE99/02339 07/29/1999 <i>shc</i>					
** FOREIGN APPLICATIONS VERIFIED: <i>None</i>					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO F02,0022	
TITLE : Method for producing integrated semiconductor components <small>U S DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)</small>					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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